PTO/SB/08 Equivalent

INFORMATION DISCLOSURE STATEMENT BY APPLICANT		
	Application No.	10/586,846
	Filing Date	July 20, 2006
	First Named Inventor	Verbauwhede et al.
	Art Unit	2819
(Multiple sheets used when necessary)	Examiner	Unknown
SHEET 1 OF 1	Attorney Docket No.	UCLARF.004NP

U.S. PATENT DOCUMENTS

Examiner Initials	Cite No.	Document Number Number - Kind Code (if known) Example: 1,234,567 B1	Publication Date MM-DD-YYYY	Name of Patentee or Applicant	Pages, Columns, Lines Where Relevant Passages or Relevant Figures Appear
	1	6,265,923 B1	07-24-2001	Amir et al.	
	2	6,329,846 B1	12-11-2001	Davies et al.	

FOREIGN PATENT DOCUMENTS

Examiner Initials	Cite No.	Foreign Patent Document Country Code-Number-Kind Code Example: JP 1234567 A1	Publication Date MM-DD-YYYY	Name of Patentee or Applicant	Pages, Columns, Lines Where Relevant Passages or Relevant Figures Appear	T[1]
	3	EP 0921639 A	06-09-1999	Hewlett-Packard Company		

NON PATENT LITERATURE DOCUMENTS

Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T[1]
	4	RUIZ G A ET AL., "Compact 32-Bit CMOS Adder in Multiple-Output DCVS Logic for Self-Timed Circuits," IEE Proceedings: Circuits Devices and Systems, Institution of Electrical Engineers, Stenvenage, GB, Volume 147, Number 3, June 8, 2000, pages 183-188	
	5	International Search Report from PCT/US2005/004877, April 10, 2006, 7 pages	
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	7		
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Examiner Signature	/Matthew Tabler/	Date Considered	01/26/2009

*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /M.T./

T[1] - Place a check mark in this area when an English language Translation is attached.